

PENDING CLAIMS

1. (Original) A circuit to access unstored data comprising:
 - a register file;
 - a first bi-directional OR controller connected to said register file;
 - a first multiplexer having a first input connected to an output of said first bi-directional OR controller;
 - a second bi-directional OR controller connected to said register file;
 - a second multiplexer having a first input connected to an output of said second bi-directional OR controller; and
 - a control circuit connected to said first bi-directional OR controller and said second bi-directional OR controller.
2. (Previously Presented) The circuit according to claim 1 further comprising:
 - a first latch having an output connected to a first input of said first bi-directional OR controller; and
 - a second latch having an output connected to a first input to said second bi-directional OR controller.
3. (Previously Presented) The circuit according to claim 2 further comprising:
 - a first functional unit having a first output connected to a second input to said first multiplexer and having a second output connected to an input to said first latch.
4. (Previously Presented) The circuit according to claim 3 further comprising:
 - a second functional unit having a first output connected to a second input of said second multiplexer and having a second output connected to an input of said second latch.
5. (Previously Presented) The circuit according to claim 4 wherein:
 - said first multiplexer is part of a first data forwarding circuit; and
 - said second multiplexer is part of a second data forwarding circuit.
6. - 13. (Canceled).

14. (Original) An encoded multiplexer comprising:
a first input with at least one instance;
a second input with at least one instance;
a first raw select signal with at least one instance;
a second raw select signal with at least one instance; and
a circuit which combines said first raw select signal and said second raw select signal
to determine which input should be used as a first conditioned select signal and a second
conditioned select signal.

15.- 17. (Canceled).

18. (Original) An encoded multiplexer comprising:
a first input with at least one instance;
a second input with at least one instance;
a first raw select signal with at least one instance;
a second raw select signal with at least one instance; and
a circuit which combines said first raw select signal and said second raw select signal
to determine which input should be used as a first conditioned select signal, a second
conditioned select signal, and a third conditioned select signal.

19. - 20. (Canceled).